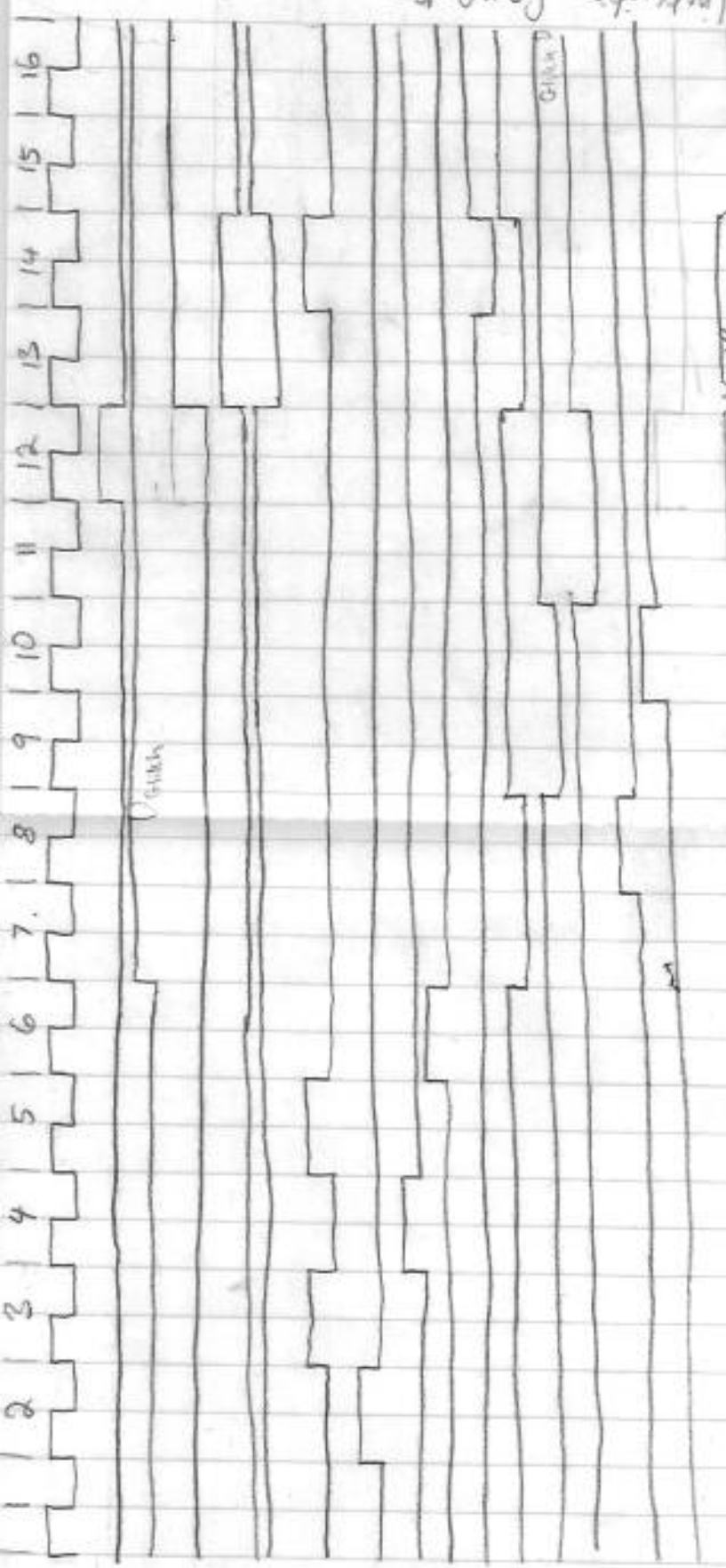


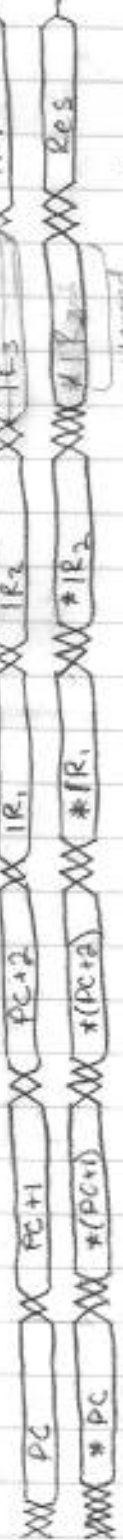
State

Clock

- A: PC_{store}
- B: $358PC$
- C: $MemRead$
- D: $PCLOAD$
- E: $Res clk$
- F: $IR_1 store$
- G: $IR_2 store$
- H: $PC store$
- J: $MemStore$
- K: $IR_1 read$
- L: $IR_2 read$
- M: $IR_3 read$
- N: $F_1 store$
- O: $F_2 store$



Address Bus
Data Bus



Note: we can tie \bar{C} and D together. Problems will not arise unless we try to use the busses after state 14.

One Instruction Computer
Mark III

2 March 2000